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	•			Application Number	10646988	
INFORMATION DISCLOSURE				Filing Date	8/22/2003	
STATEMENT BY APPLICANT			T	First Named Inventor	Darius D. Gaskins	
(use as many sheets as necessary				Group Art Unit	2115	
				Examiner Name	Mark A. Connolly	
Sheet	1	of	1	Attorney Docket Number	CNTR.2209	

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/MC/	AB	Intel 64 and IA-32 Architectures Software Developer's Manual. Volume 3A: System Programming Guide, Part 1. November 2006. Pages 13-1, and 13-5 to 13-15.					
/MC/	AC	Intel Pentium M Processor with 2-MB L2 Cache and 533-MHz Front Side Bus. Datasheet. July 2005. Pages 14 and 62-64. Reference Number 305262-002					
/MC/	AD	Wireless Intel SpeedStep Power Manager. "Optimizing power consumption for the Intel PXA27x processor family." Intel. 2004. Pages 1-16.					
/MC/	AE.	"Enhanced Intel SpeedStep Technology for the Intel Pentium M Processor." Intel. March 2004. Pages 1-12					
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